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OFFICE OF PETITIONS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Tak H. Ning

Examiner: Thien Tran

Serial No: 09/757,965

Art Unit: 2811

Filed: December 21, 2001

Docket: YOR920000588US2(16966)

For: FULLY DEPLETED COLLECTOR
SILICON ON INSULATOR BIPOLAR
TRANSISTOR USEFUL ALONE OR IN
SOI BiCMOS

Dated: November 11, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. 1.111

Sir:

Applicant submits the following amendments and remarks for entry of record
in the above-identified patent application.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United
States Postal Service as first class mail in an envelope addressed to: Commissioner for
Patents, Box 1450, Alexandria, VA 22313-1450 on November 11, 2003

Dated: November 11, 2003



Leslie S. Szivos, Ph.D.